ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

DYNAMICALLY PATTERNED SHIELDED HIGH-Q INDUCTOR

Application Number:

Confirmation Number:

First Named Applicant:

Jae-Eun Park

Attorney Docket Number:

FIS920030075US1

Art Unit:

Examiner:

Search string:

(5151775 or 5760456 or 6310387 or 20020074620).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
TT	1	5151775	1992-09-29	Hadwin			
TT	2	5760456	1998-06-02	Grzegorek, et al.			
11	3	6310387	2001-10-30	Seefeldt, et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
TT	1	20020074620	2002-06-20	Yue		-	

Signature

I Examiner Name	Date !
Wen WALL	08/22/04
TVUTI WINL	00100104

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					Docket Number (Option FIS920030075)	Application Number					
					Applicant(s) Jae-Eun Park, et al						
SEP	2 2 7	na E			Filing Date			Group Art Unit			
Vien.	Danov ^a	47Kg	U.	S. PATENT	DOCUMENTS						
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	-		FOR	EIGN PATE	NT DOCUMENTS						
	R	DOCUMENT NUMBER	DATE		COUNTRY	CLASS	SUBCLASS	YES	NO		
TT		EP0536972B1	14/4/93	Europe				Х			
TT		JP10208940A	7/8/98	Japan				Х			
TT		WO 02/49110 A1	20/6/02	PCT		7//		X			
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TI		"On-Chip Spira Circuits", A I	l Induct	ors Fo	r Silicon-Ba	ased Radio		y Integ	rated		
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Form PTO-A820 (also form PTO-1449)